



Agilent E1445A

# **Description**

The Agilent E1445A Arbitrary Function Generator is a **C-size**, **1-slot**, **message-based VXI module**. It provides the flexibility to produce virtually any waveform needed.

The deep memory allows downloading a large number of waveforms at once, and can store up to 128 waveforms using SCPI programming. The memory sequencer lets you link waveform segments together in any order. These sequences can be repeated 1 to 64 k times or continuously. Within a sequence, the segments can be repeated up to 4,096 times using only one sequence memory entry. This memory structure lets you build large, complex waveforms out of small segments.

Refer to the Agilent Technologies Website for instrument driver availability and downloading instructions, as well as for recent product updates, if applicable.

# **Agilent E1445A Arbitrary Function Generator**

Data Sheet

- 1-Slot, C-size, message based
- 13-bit resolution, 40 MSa/s
- 256 kSa waveform segment memory
- Waveform and frequency hopping with sweep function
- Direct access to high-speed registers
- Built-in self-test

# **Produce Complex Waveforms**

Essentially, there are two memories built into the E1445A:

- 1. 256 kSa segment memory that supplies the digital-to-analog converter (DAC) with its output values; and
- 2. 32 k-segment sequence memory that defines how the segments are consecutively linked together at full speed.

The memory sequencer lets you link waveform segments together in any order. These sequences can be repeated 1 to 64 k times or continuously. Within a sequence, the segments can be repeated up to 4,096 times using only one sequence memory entry. This memory structure lets you build large, complex waveforms out of small segments.

# **Precisely Control the Frequency**

One of the clocks is created by the Direct Digital Synthesis (DDS) technique. With DDS, you get very high resolution. This allows you to precisely set the frequencies you need.

For signals with the lowest phase noise, crystal oscillators with divider circuits are also on-board to clock the DAC. This allows you to set values like 20 MSa/s with minimal jitter.

# **Hop Frequencies**

Frequency hopping is done easily by programming a list of frequencies and instructing the internal microprocessor to step through the list. As an added benefit, the frequency changes are phase continuous. Using this feature, you can produce bursts of several tones.

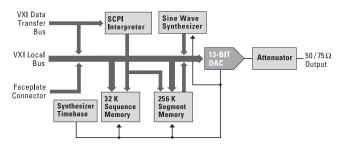
# **Drive the DAC Directly**

When you have an extremely long or indeterminate waveform, you can use the VXI Local Bus or the faceplate connector to drive the DAC directly. This lets your process define the waveform being produced by the E1445A. Local Bus speed is limited to 7.4 MSa/s typical. Neither is paced by the internal time base, they must be paced externally.



# **Control and Synchronize Other Instruments**

A programmable marker places a pulse on the Marker Out BNC. This marker can appear in any location in the segment memory. You can use the marker to synchronize other instruments, such as an oscilloscope or a digital functional tester.



# **Product Specifications**

#### Waveforms

**Arbitrary waveform** 

function:

Standard waveforms: Sine, square, ramp, and triangle

13 bits (12 bits for sine) Resolution:

Sample rate generation

method:

Direct digital synthesis (DDS) or time base

sources with digital dividers

# Sample rate using DDS:\*

Mode:	Resolution	Range (Sa/s):
DDS normal	0.01 Sa/s	0.01 to 10.7 M
DDS doubled	0.02 Sa/s	0.02 to 21.4
* Internal 42.94 MHz crystal		M

(Resolution using non-DDS timebase) (time Sample rate:

256 using SCPI

128 using SCPI

base frequency)/(divider), divider = 1, 2, 3,

2N (N = 1 to 64 k), max. 40 MSa/s

Waveform segment

memory: 256 kSa

Maximum number of

segments:

Sequence memory: 32,768 seaments Maximum number of

waveforms in memory: Waveform sequence

looping (burst output

mode): 1 to 65,536 cycles or continuous

Segment looping: 1 to 4.096

Programmed in memory or randomly using Waveform hopping: register access via VXI Data Transfer Bus

(P1), VXI Local Bus (P2), or faceplate connector

**Modulation:** FSK, PM **Frequency Rates** 

Sample rate: 40 MSa/s

Time base sources: Internal 40 MHz and 42.9 MHz crystals (50 ppm); VXI CLK10 line; VXI ECLTrig lines;

faceplate BNC

10.7 MHz sine, 5 MHz square, 100 kHz ramp/ **Maximum waveform** 

frequency: triangle using 100 samples per cycle

Sweep: Linear and log frequency Frequency sweep range: 0.01 Hz to 10 MHz 0.01 Hz to 10 MHz Frequency hop range:

Frequency hop rate: Up to 500 kHz using registers, 800 Hz using

SCPI

Frequency shift (FSK) rate: Up to 2 M changes/s Phase modulation rate: Up to 500 kHz

Phase modulation source: Software, VXI Local Bus (P2), or faceplate

connector

Square waveform rise

time: 17 ns typical

**Output** 

Amplitude: ± 10.2 V max. (open circuit)

50 or 75  $\Omega$  (output also calibrated for open **Output impedance** 

(software selectable): circuit)

Voltage amplitude range:  $\pm$  5.1 V in 1.25 mV steps in 50  $\Omega_{\rm r}$   $\pm$  10.2 V

in 2.5 mV steps in to high impedance.

Monotonicity: >11 bits

Differential nonlinearity (dc):

4 LSB

Amplitude accuracy (dc):  $\pm$  (0.3% + 5 mV) into 50  $\Omega$ 

Output

Maximum offset:  $\pm$  5 V into 50  $\Omega$ 

Maximum output:  $\pm$  5.5 V AC+DC into 50  $\Omega$ 

 $\pm$  (0.1 dB + attenuator error + ac flatness) Amplitude accuracy (ac):

(Absolute)

Sine total harmonic distortion with internal filters

applied:

**Harmonic Level Frequency Range** 0.1 - 250 kHz \_60 dBc

0.25 - 4 MHz  $-60 \text{ dBc} + 20 \log (f/250 \text{ k})$ 

4 MHz - 10 MHz -36 dBc

Note: f = output frequency

Sine spurious nonharmonic distortion:

Frequency Range	Non-harmonic Level
10 Hz - 1 MHz	-60 dBc or -60 dBm, (whichever is greater)
1 MHz - 4 MHz	-50 dBc
4 MHz - 10 MHz	–45 dBc

#### AC flatness:

#### **Frequency Range Flatness**

0.1 Hz - 100 kHz 0.05 dB 100 - 250 kHz 0.1 dB 0.2 dB 1 kHz - 10 MHz

Note: relative to 1 kHz with internal filters

Attenuator range: 0 to 30 dB in 0.01 steps

0 dB at max output level, 0.05 dB at other Attenuator error:

levels

**Output filters** 

250 kHz, 5-pole Bessel; 10 MHz, 7-pole (software selectable):

Bessel; no filter applied

# **Auxiliary Input/Output**

**VXI Local Bus:** Data to DAC (not synchronized to time base

> and limited to 7.4 MSa/s typical), data to segment memory, waveform selection, phase

modulation

Auto, hold, software, VXI TTLTRG, VXI **Trigger sources:** 

ECLTRG, or faceplate BNC

# **Faceplate Connectors**

Ref/sample in BNC: Frequency reference, sample clock

Start arm in BNC: Start arm

Stop trig/FSK/gate in BNC:

Marker out:

**Digital port:** 

Trigger clock gate, Trigger stop, FSK Any point, start of sequence, sample clock,

reference frequency, frequency/phase change Data to DAC or segment memory, waveform

selection, phase modulation

**VXI TTLTRG lines:** Sample clock, gate, sweep arm/trigger, FSK

**VXI ECLTRG lines:** Sample clock, reference frequency, start arm,

all marker outputs

# **General Specifications**

#### **VXI Characteristics**

VXI device type: Message based

Data transfer bus: A16, A32, D8/16/32 slave

only С

Size: Slots: P1/2 **Connectors: Shared memory:** 

VXI busses: Local Bus A-row, Local Bus

C-row, TTL Trigger Bus, ECL

Trigger Bus

C-size compatibility:

#### **Instrument Drivers**

See the Agilent Technologies Website (http://www.agilent.com/find/ inst\_drivers) for driver availability and downloading.

**Command module** 

firmware: n/a **Command module** firmware rev: n/a I-SCPI Win 3.1: n/a I-SCPI Series 700: n/a C-SCPI LynxOS: n/a C-SCPI Series 700: n/a **Panel Drivers:** Yes VXI*plug&play* Win Framework: No VXI plug&play Win 95/NT Framework: Yes

VXI*plug&play* HP-UX

Framework: No

### **Module Current**

	I <sub>PM</sub>	I <sub>DM</sub>	
+5 V:	3.5	0.2	
+12 V:	0.1	0.1	
–12 V:	0.13	0.06	
+24 V:	0.22	0.17	
–24 V:	0.34	0.17	
−5.2 V:	2.5	0.12	
–2 V:	1.2	0.2	

# Cooling/Slot

Watts/slot: 44.00  $\Delta P \text{ mm H}_20$ : 0.50 Air Flow liter/s: 3.50

# **Ordering Information**

Description	Product No.
C-Size Arbitrary Function Generator	E1445A
Service Manual	E1445A 0B3
Germany - German Localization	E1445A ABD
France - French Localization	E1445A ABF
Japan - Japanese Localization	E1445A ABJ
Backplane Connector Shield Kit	E1400-80920

#### **Related Literature**

2000 Test System and VXI Catalog CD-ROM,
Agilent Pub. No. 5980-0308E (detailed specifications for VXI products)

2000 Test System and VXI Catalog, Agilent Pub. No. 5980-0307E (overview of VXI products)

1998 Test System and VXI Products Data Book, Agilent Pub. No. 5966-2812E

#### **Online**

Internet access for Agilent product information, services and support www.agilent.com/find/tmdir

VXI product information www.agilent.com/find/vxi

Defense Electronics Applications www.agilent.com/find/defense ATE

Agilent Technologies VXI Channel Partners www.agilent.com/find/vxichanpart

Agilent Technologies' HP VEE Application Website www.agilent.com/find/vee

Agilent Technologies Data Acquisition and Control Website www.agilent.com/find/data\_acq

Agilent Technologies Instrument Driver Downloads www.agilent.com/find/inst drivers

Agilent Technologies Electronics Manufacturing Test Solutions www.agilent.com/go/manufacturing

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#### Phone or Fax

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(tel) (82 2) 2004 5004 (fax) (82 2) 2004 5115

### Latin America:

(tel) (305) 269 7500 (fax) (305) 269 7599

#### Taiwan:

(tel) 0800 047 866 (fax) 0800 286 331

#### **Other Asia Pacific Countries:**

(tel) (65) 6375 8100 (fax) (65) 6836 0252 (e-mail) tm\_asia@agilent.com

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